

FORM PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION (Use several sheets if necessary)	Docket Number (Optional) 40020852-02		Application Number Not yet assigned	
	Applicant Chikamatsu			
	Filing Date herewith		Group Art Unit Not yet assigned 2857	

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
JRW	4,888,701	12/19/89	Wakasugi et al.	708 364	65 482	

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
JRW	09-281163	31.10.97	Japan			X	
JRW	05-119078	14.05.93	Japan			X	
JRW	50-133423	10.4.74	Japan				X

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, Etc.)

JRW	Crols, J. et al. "LOW-IF TOPOLOGIES FOR HIGH-PERFORMANCE ANALOG FRONT ENDS OF FULLY INTEGRATED RECEIVERS," IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 45, No. 3, March 1998.

EXAMINER 	DATE CONSIDERED 09/06/05
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP §609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.